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	THOM!	amed I	Inventor:	Isaiah O. Olade	ji	Group Art Unit: 2827			
Ni-E.	Serial	No.	10/699	,974		Examiner:			
	Filed:		11/02/2	2003					
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]	INFORM	MATION DISCL	OSURE STATEM	IENT TRANSMITTAL			
	\boxtimes	1.		nt to 37 CFR 1.97(3 months of filing	b) s or prior to 1st Offi	ce Action)			
		2.		cation Pursuant to Final Office Action	, -				
		3.		n, Certification & Instruction in the Instruction i	-	nt Pursuant to 37 CFR 1.97(d)			
	Mail Stop Patent Application Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450								
	Sir:								
	The following are submitted in the above-identified application in compliance with CFR 1.97 & 1.98:								
	\boxtimes	4.	A list o	of documents on fo	rm PTO-1449 is en	nclosed herewith.			
	This p	aper is	submitte	d in accordance w	ith:				
	\boxtimes	5.	37 CFF	R 1.97(b): [within 3	3 months of filing o	or prior to 1st Office Action]			
		6.	37 CFF and	R 1.97(c): [before	Final office Action	n or Allowance, whichever is earlier;			
		7.	The rec	quired certification	made in item 10 b	elow; or			
		8.		80.00 fee specified sure Statement.	d in 37 CFR §1.17	(p) for submission of this Information			
		9.	37 CFF	R §1.95(d): [before	issue fee payment]; and			

Application No. 10/699,974 This is a petition for consideration of the subject Information Disclosure a) Statement. The petition fee (\$180.00) required by 37 CFR 1.17(i)(1) is authorized in item 14 below. (Direct this letter to: "Attention: PETITIONS EXAMINER" and, if applicable, include batch locator information: e.g., "Allowed Files, Batch ____, Date of Allowance ____"), and The required Certification is stated in item 11 below. b) Certification 10. Each item of information contained in this Statement was cited in a 11. communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement; or 12. No item of information contained in this Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing this document after making reasonable inquiry, was known to any individual designated in 37 CFR 1.56(c) more than three (3) months prior to the filing of this Statement. 13. Please charge all applicable fees associated with the submittal of this Information

An original and two (2) copies of this document are enclosed.

Disclosure Statement to Deposit Account No. __

Respectfully submitted,

James H. Beusse, Esquire

Rég. No. 27,115

Beusse Brownlee Wolter Mora & Maire, P.A.

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Orlando, FL 32801

Telephone: (407) 926-7701 Facsimile: (407) 926-7720 Email: jbeusse@iplawfl.com JUN 2 1 2004

CERTIFICATE OF MAILING

HEREBY CERTIFY that this Information Disclosure Statement is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Mail Stop Patent Application, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 16th day of June, 2004.

Shirley Goff

PTO/SB/08A (08-03)

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Substitute for form 1449/PTO

Sheet 1

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number 10/699,974

Filing Date 11/02/2003

First Named Inventor Isaiah O. Oladeji
Art Unit 2827

Examiner Name

Attorney Docket Number Jessen 12-4-6 (284)

			U. S. PATENT	DOCUMENTS		
Examiner Initials*	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	_	Number-Kind Code ^{2 (# known)}	0010410004		rigates Appear	
	1	^{US-} 2001/0004550	06/21/2001	Passemard		
	2	^{US-} 2003/0100190	05/29/2003	Cote et al.		
	3	^{US-} 5,635,423	06/03/1997	Huang et al.		
	4	^{US-} 5,821,169	10/13/1998	Nguyen et al.		
	5	^{US-} 6,087,250	07/11/2000	Hyakutake		
	6	^{US-} 6,143,646	11/07/2000	Wetzel		
	7	^{US-} 6,150,073	11/21/2000	Huang		
	8	^{US-} 6,156,643	12/05/2000	Chan et al.		
	9	^{US-} 6,221,759	04/24/2001	Bothra et al.		
	10	^{US-} 6,225,217	05/01/2001	Usami et al.		
	11	^{US-} 6,255,735	07/03/2001	Wang et al.		
_	12	^{US-} 6,312,874	11/06/2001	Chan et al.		
	13	^{US-} 6,468,898	10/22/2002	Usami		
	14	^{US-} 6,479,391	11.12.2992	Morrow et al.		
	15	^{US-} 6,507,081	01/14/2003	Smith et al.		
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FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages			
		Country Code ³ "Number ⁴ "Kind Code ⁵ (if known)	MM-DD-YYYY		Or Relevant Figures Appear	T ⁶		
	16	WO 01/99184 A2	12/27/2001	Infineon Technologies				
	17	WO 01/43171 A1	06/14/2001	Infineon Technologies				
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Examiner		Date	
Signature	•	Considered	

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	ite for form 1449/PTO			Complete if Known		
Gusan				Application Number	10/699,975	
INF	ORMATION	DIS	CLOSURE	Filing Date	11/02/2003	
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Sheet	2	of	2	Attorney Docket Number	Jessen 12-4-6 (284)	

Evaniaca	Cito	NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	18	S. WOLF and R.N. TAUBER, Silicon Processing for VLS/Era, Volume 1 - Process Technology, pp. 797-799, Second Edition (2000) Published by Lattice Press, Sunset Beach, CA	
	19	VINCENT ARNAL et al., A Novel SIO2-Air Gap Low K For Copper Dual Damascene Interconnect, Advanced Metalization Conference 2000, D. Edelstein et al., Warrendale, PA, USA, pp. 71-76	
	20	S. PURUSHOTHAMAN et al., Opportunities and Challenges in Ultra Low k Dielectrics for Interconnect Applications, IEEE, 2001, pp. IEDM 01-529-532	
	21	LYTLE et al., Overcoming Cu/CVD Low-k Integration Challenges in a High Performance Interconnect Technology, IEEE 2001, pp. IEDM 01-611-614.	
	22	ZIELINSKI et al., Damascene Integration of Copper and Ultra-Low-k Xerogel For High Performance Interconnects, 1997, pp. IEDM 97-936-938.	
	23	ARNAL et al., Integration of a 3 Level Cu-SiO2 Air Gap Interconnect for Sub 0.1 micron CMOS Technologies, 2001 IEEE, pp. 298-300.	
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Examiner	Date	
Signature	Considered	

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Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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